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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **WATANABE, Kenichi**

Group Art Unit: **2815**

Serial No.: **10/050,171**

Examiner: **WARREN, Matthew E.**

Filed: **January 18, 2002**

P.T.O. Confirmation No.: **7623**

FOR: **SEMICONDUCTOR DEVICE CAPABLE OF SUPPRESSING CURRENT CONCENTRATION IN PAD AND ITS MANUFACTURE METHOD**

**RESPONSE UNDER 37 CFR §1.116**

**- EXPEDITED RESPONSE -**

**GROUP ART UNIT 2815**

**MAILSTOP AF**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

February 1, 2007

Sir:

In response to the Office Action dated November 2, 2006, please amend the above-identified application as follows:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 11 of this paper.